

Docket No.: 2207/9048

ABSTRACT OF THE DISCLOSURE

5 In order to recover phase information, data transmitted at a first frequency is over-sampled using a clock at a second frequency,  $n$  times per bit time to obtain  $n$  samples. The  $n$  samples are used to detect the transitions between two logic levels in said transmitted data which are stored in groups of  $m$  sets of said  $n$  edge results which are, in turn output at a clock frequency which is the second frequency divided by  $m$ , for further processing.

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